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**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR**  
(AUTONOMOUS)

**B.Tech III Year II Semester Regular Examinations May 2019**

**OBJECT ORIENTED ANALYSIS AND DESIGN**

(Computer Science and Engineering)

Time: 3 hours

Max. Marks: 60

(Answer all Five Units **5 x 12 = 60** Marks)

**UNIT-I**

- 1 a Describe the principles of modeling. 6M  
b Describe the behavioral and grouping things in the UML. 6M

**OR**

- 2 a Explain the common mechanisms in the UML. 7M  
b Explain the building blocks of the UML. 5M

**UNIT-II**

- 3 Describe the terms and concepts of classes. 12M

**OR**

- 4 Explain Stereotypes, tagged values, note and constraints with examples. 12M

**UNIT-III**

- 5 a Explain the terms and concepts of Class diagram with suitable examples. 7M  
b Explain the Forward and Reverse Engineering 5M

**OR**

- 6 a Explain in details of Use cases, actors, include, and extend with suitable example. 7M  
b Explain in details of Use case diagram with suitable example. 5M

**UNIT-IV**

- 7 a What are States, transitions, and activities? 7M  
b Common modeling techniques for processes and threads. 5M

**OR**

- 8 a Explain the terms and concepts of state chart diagrams with suitable example. 6M  
b Draw a state chart diagram for the library management system. 6M

**UNIT-V**

- 9 Explain the terms and concepts of Component diagrams with example. 12M

**OR**

- 10 Define deployment diagram. Explain the modeling techniques of deployment diagrams. 12M

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